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| (Signature | $\overline{}$ | M | | 3 | |
| (Date | 0 | 2006 | 28, | JULY | |
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/826.725 | 04/15/2004 | Chun Hsiang Lai | JCLA6643-D | 1368 |

TITLE OF INVENTION: ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT COUPLED ON I/O PAD

| APPLN, TYPE | SMALL ENTITY | ISSUE FEE | | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|--|---|--|---|---|-------------------------------------|--|
| nonprovisional | NO | \$1400 | | \$300 | \$1700 | 08/02/2006 |
| EXAMINER | | ART UNI | т | CLASS-SUBCLASS | | |
| PATEL, DHA | RTI HARIDAS | 2836 | | 361-056000 g7/3 | 11/2006 TRESHAH2 00001 | 3052 10826725 |
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| a. Applicant claims S | (from status indicated above MALL ENTITY status. See is requested to apply the Issurablication Fee (if required) words of the United States Pate | 37 CFR 1.27. | | ant is no longer claiming SMAI y) or to re-apply any previously c other than the applicant; a regi | | |
| Authorized Signature Typed or printed name | JIAWEI HU | M | | Date | 07/28/2006 To. 43,330 | 5 |

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